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CONFIRMATION NO. 6340

SERIAL NUMBER 10/737,413	FILING DATE 12/15/2003 RULE	CLASS 365	GROUP ART UNIT 2824	ATTORNEY DOCKET NO. JCLA11768
APPLICANTS Jhyy-Cheng Liou, Jhubei City, TAIWAN;				
** CONTINUING DATA ***** None JA				
** FOREIGN APPLICATIONS ***** None JA				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/03/2004		** SMALL ENTITY **		
Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 8	TOTAL CLAIMS 21
Verified and Acknowledged Examiner's Signature	Initials			INDEPENDENT CLAIMS 3
ADDRESS 23900 J C PATENTS, INC. 4 VENTURE, SUITE 250 IRVINE , CA 92618				
TITLE Non-volatile semiconductor memory array structure and operations				
FILING FEE RECEIVED 394	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		

10/737413

Examiner: HUR, JOHN

GAU: 2824

Classification: 365/185.050

Inventor: LIOU, JHYY-CHENG

Status: 30 - DOCKETED NEW CASE - READY FOR EXAMINATION

Title: NON-VOLATILE SEMICONDUCTOR MEMORY ARRAY STRUCTURE AND OPERATIONS

Bib Data report

**Application Title: NON-VOLATILE SEMICONDUCTOR
MEMORY ARRAY STRUCTURE AND OPERATIONS**

**Application Num:  (in phx) 10737413 Filing Date:
12/15/2003 Effective Filing: 12/15/2003
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**Examiner: 79441 HUR, JOHN (Assignment Data)
Group Art Unit: 2824 Class/Subclass: 365/185.050**

**State or Country: TAIWAN Sheets/Drawing: 8 Total
Claims: 21 Independent Claims: 3**

▼ Inventors:

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City:

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**Country or
State:**

TAIWAN

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Attorneys: ALL Attorney Docket No: JCLA11768

**Interference No: Lost Case: No Unmatched Petition:
No L&R Code: 1**